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Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/608,932
				Filing Date	27 June 2003
				First Named Inventor	Robinson
				Art Unit	2819
				Examiner Name	Lam T. Mai
				Attorney Docket Number	NG(ST)6422
Sheet	1	of	1		

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FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ²
		Country Code-Number-Kind Code (If Known)				
EXAMINER SIGNATURE			DATE CONSIDERED			

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE
(REV. 6-89) PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.: NG(ST)-6422

SERIAL NO.

10608932

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

APPLICANT: Ian S. Robinson, et al.

(Use several sheets if necessary)

FILING DATE: herewith

GROUP:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>J</i>	6	4	3	7	7	1	8	8/20/02	Oyama, et al.			
<i>D</i>	6	4	3	7	7	1	9	8/20/02	Noro, et al.			
<i>U</i>	6	4	3	8	4	3	4	8/20/02	Kamiya			
<i>W</i>	6	4	8	0	1	2	9	11/12/02	Melanson			
<i>L</i>	6	4	9	6	1	2	7	12/17/02	Olson, et al.			
<i>A</i>	6	4	9	8	5	3	5	12/24/02	Allen, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER										DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
															YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>J</i>	1	Yunfeng Liang, and Yong Lian, "A 250MHz CMOS Bandpass Delta Sigma Modulator Using Continuous-Time Resonator Structure (Short Paper)", (date unknown) "NO MONTH" "NO YEAR"

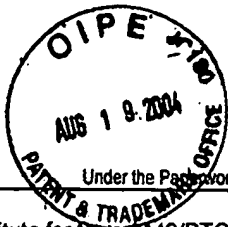
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6/2/05

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PTO/SB/D8A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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		Art Unit	2819		
		Examiner Name	Unknown		
Sheet	1	of	2	Attorney Docket Number	NG(ST)6422

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No. ¹	Document Number Number-Kind Code ²⁽¹⁾ (Known)	Publication Date MM-DD-YYYY	Name Of Patentee Or Applicant Of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear
<i>SD</i>		US-5,541,864	06-30-1996	Van Bavel, et al.	
<i>SD</i>		US-5,563,535	10/08/1996	Corry, et al.	
<i>SD</i>		US-5,859,605	01/12/1999	Raghaven, et al.	
<i>SD</i>		US-6,323,795 B1	11/27/2001	Yang, et al.	
<i>SD</i>		US-6,339,621 B1	01/15/2002	Cojocar, et al.	
<i>SD</i>		US-6,459,743 B1	10/01/2002	Lipka	
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		Country Code-Number-Kind Code (If Known)				
SD		EP 1 257 063 A2	11-13-2002	Texas Instruments Incorporated		
EXAMINER SIGNATURE Sam Ma				DATE CONSIDERED 6/3/05		

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				Examiner Name	Unknown
Sheet	2	of	2	Attorney Docket Number	NG(ST)6422

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
A		European Search Report completed 22 July 2004 for European Application No. 03025731.5-2206	
A		VILLE EEROLA, et al., "Second-Order Sampling and Oversampled A/D- and D/A-Converters in Digital Data Transmission", Analog, Circuits and Neural Networks. Singapore, June 11-14, 1991, Proceedings of the International Symposium on Circuits and Systems, New York, IEEE, US, vol. VOL. 3 SYMP. 24, 11 June 1991 (1991-06-11), pages 1521-1524, XP000616882, ISBN: 0-7803-0050-5	

Examiner Signature	<i>[Signature]</i>	Date Considered	6/13/05
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